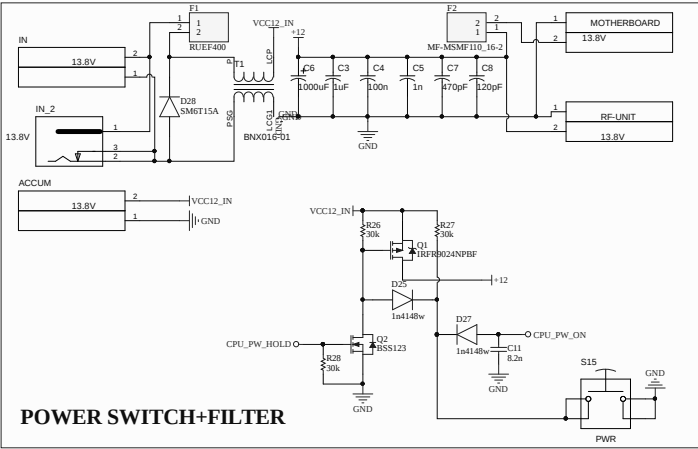
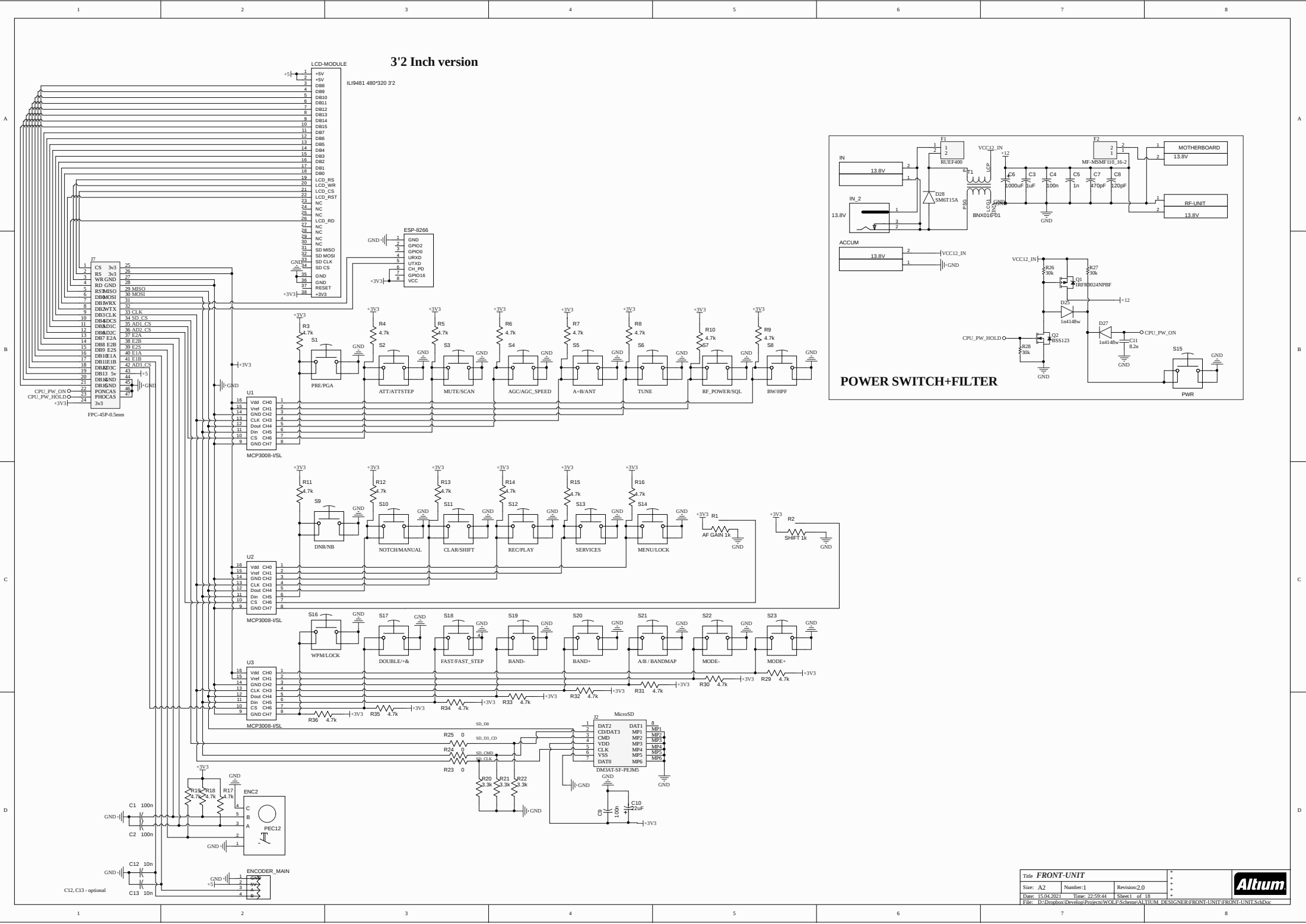
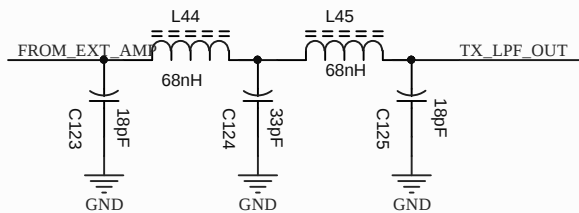


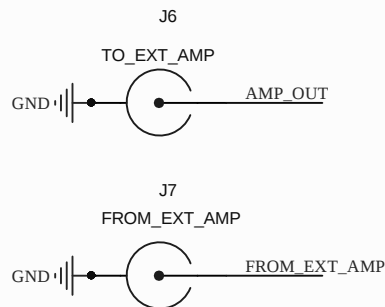


TX_LPF

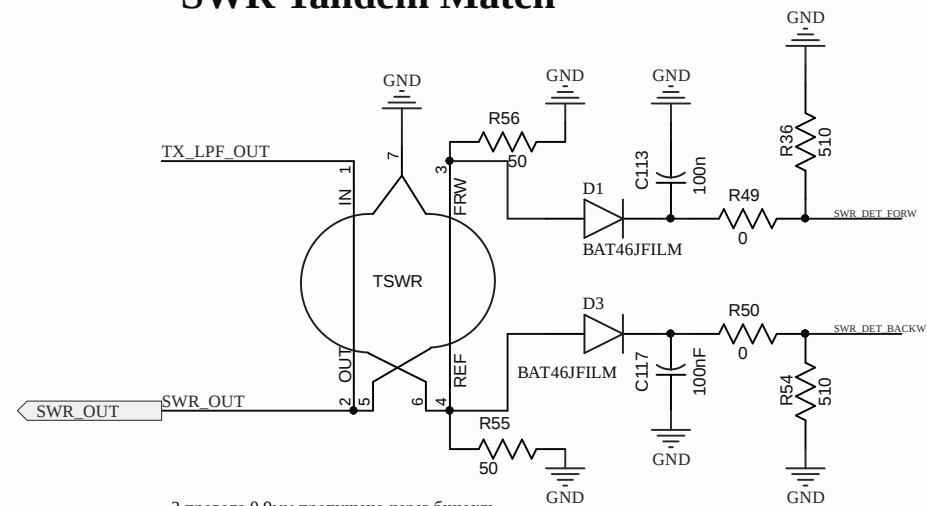
LPF 170Mhz



Катушки намотаны проводом 0.5мм на оправке диаметром 5мм, 3.5 витка



SWR Tandem Match

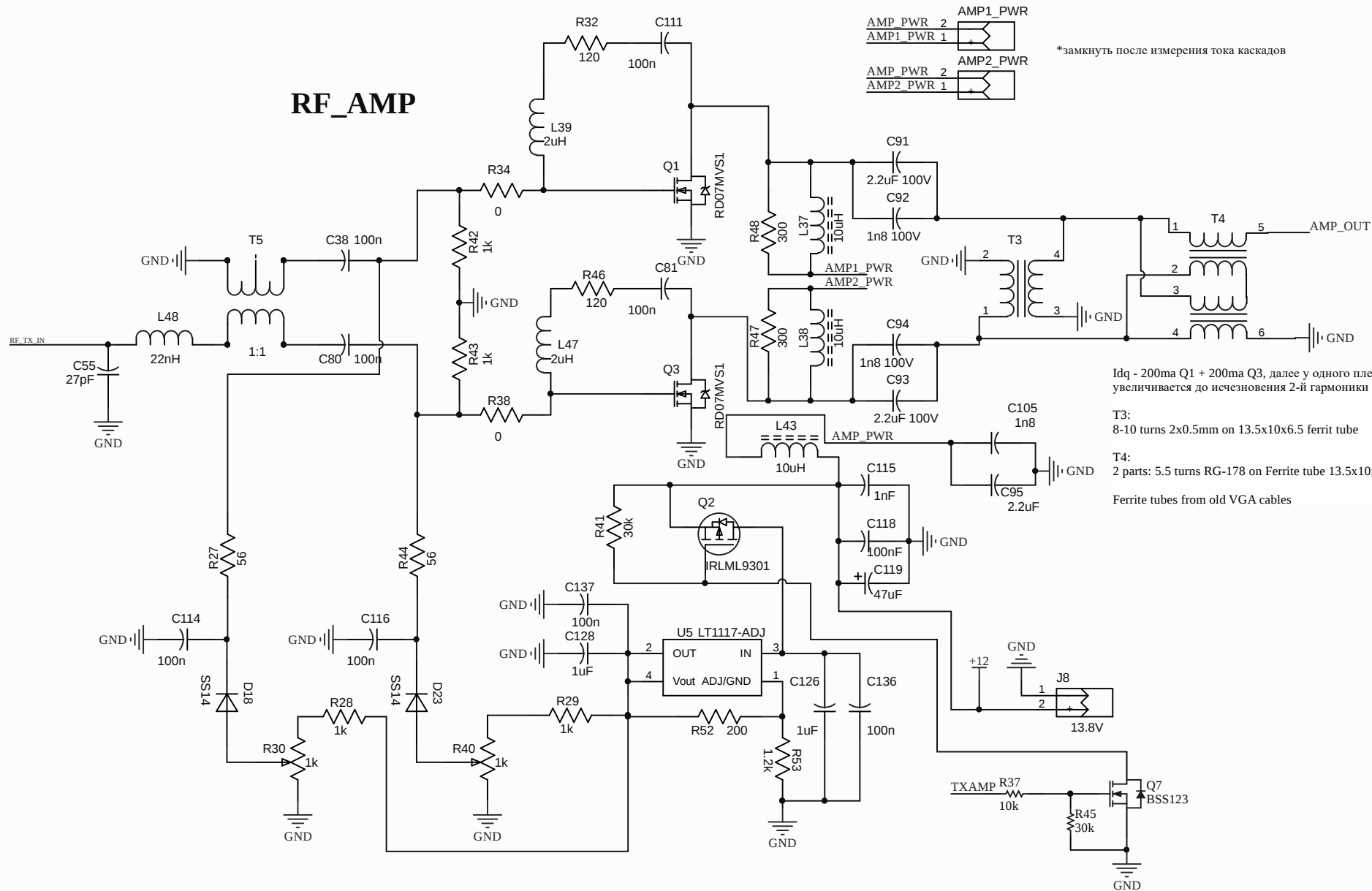


2 провода 0.9мм пропущено через бинокль

по диагонали пропущено и намотано по 10 витков 0.5мм

Title SWR Tandem Match			**
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RF_AMP



*замкнуть после измерения тока каскадов

Idq - 200ma Q1 + 200ma Q3, далее у одного плеча увеличивается до исчезновения 2-й гармоники

T3:
8-10 turns 2x0.5mm on 13.5x10x6.5 ferrite tube

T4:
2 parts: 5.5 turns RG-178 on Ferrite tube 13.5x10x6.5 each

Ferrite tubes from old VGA cables

Title **RF_AMP**

Size: A4

Number:17

Revision:2.0

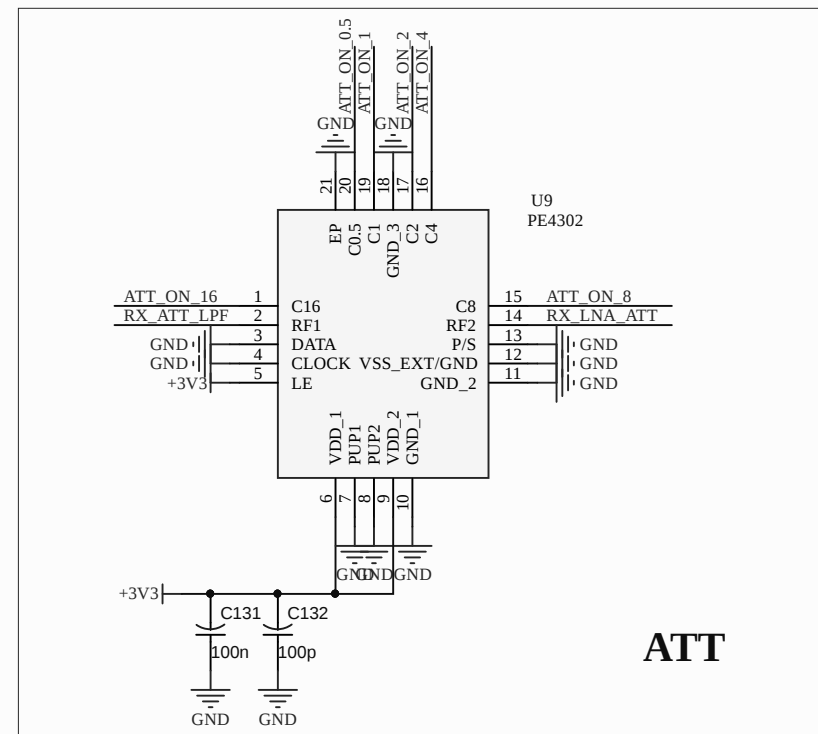
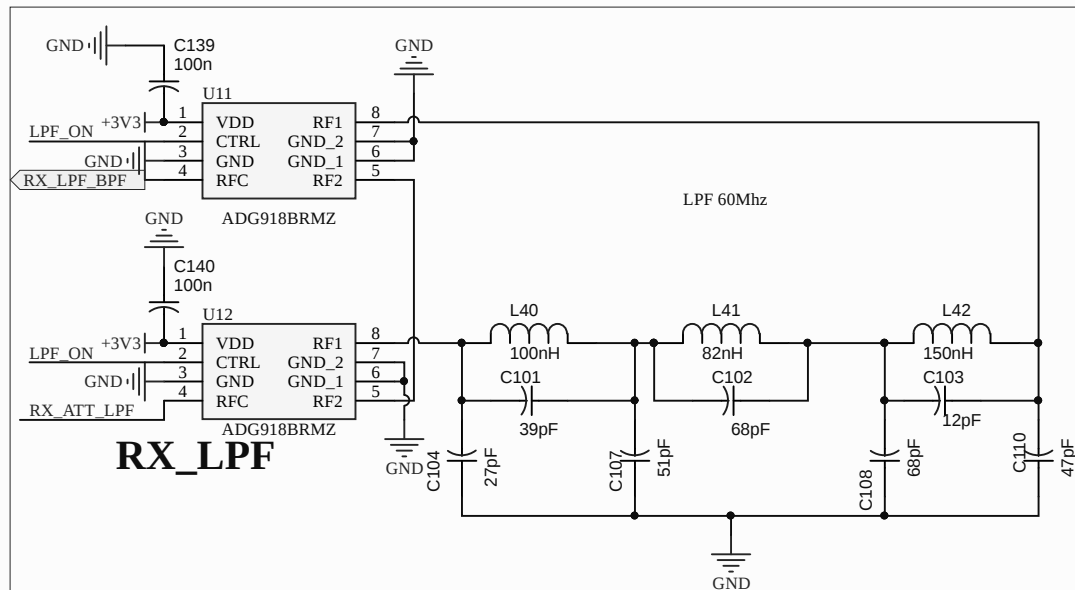
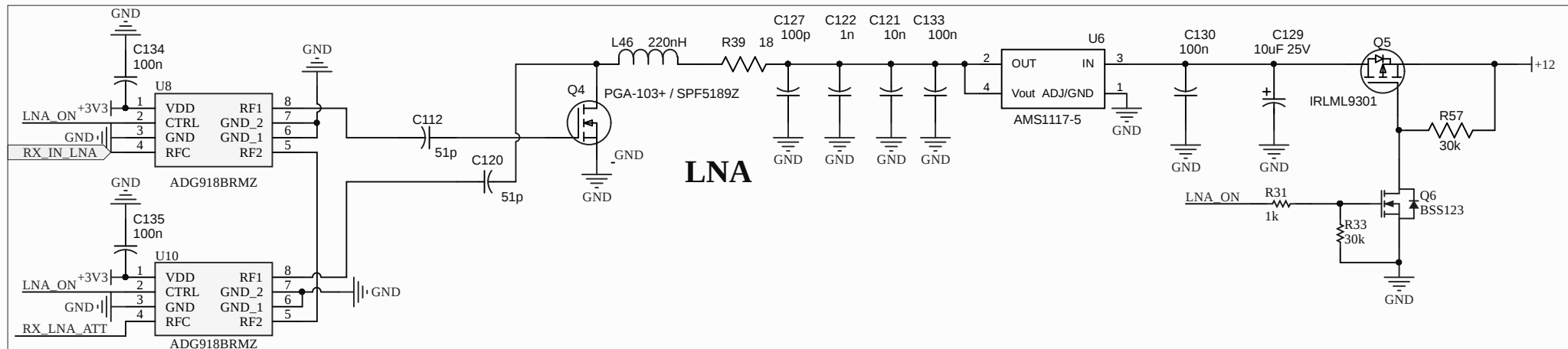
Date: 15.04.2021

Time: 22:59:48

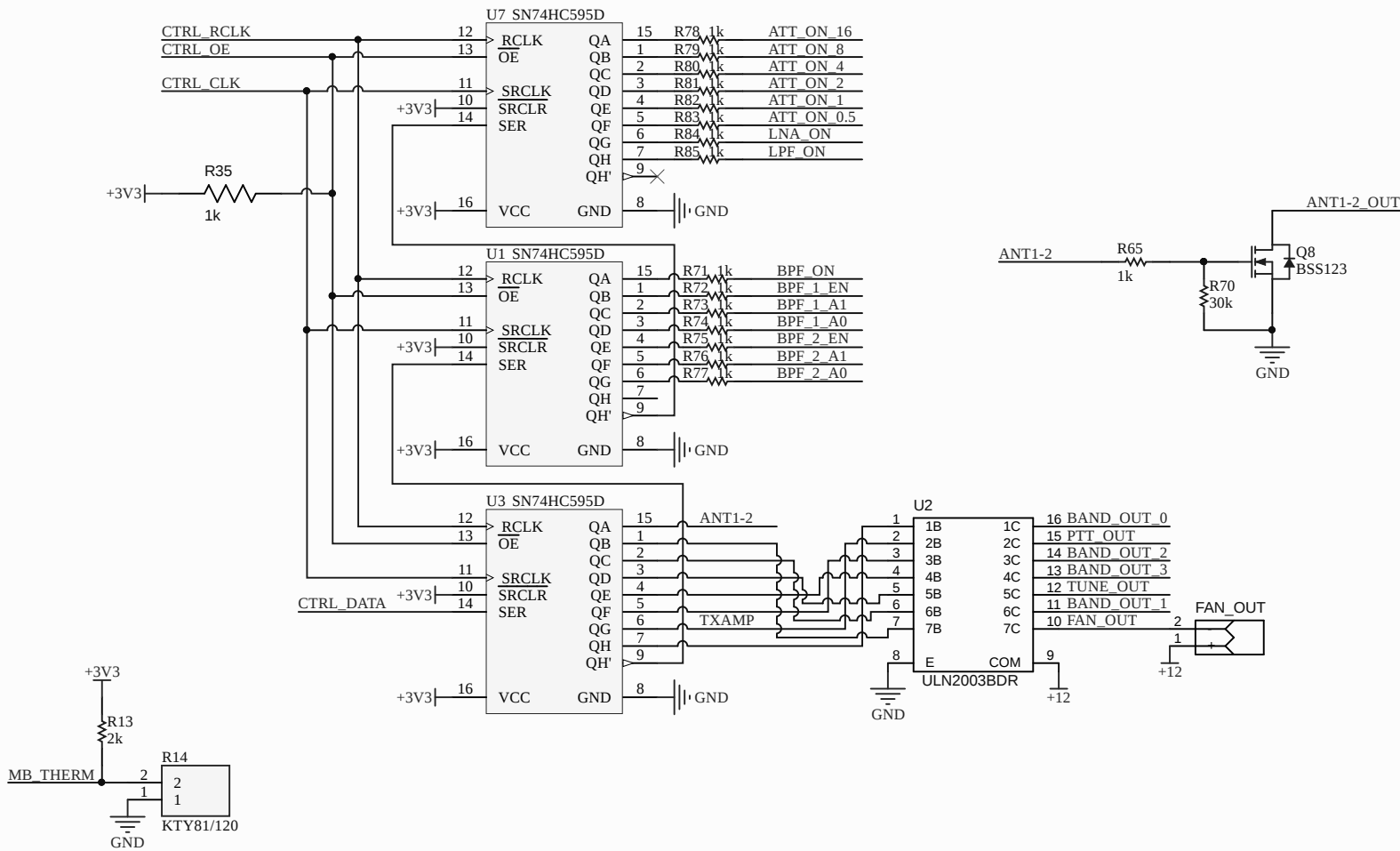
Sheet17 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTUM_DESIGNER\RF-UNIT\RF_AMP.SchDoc



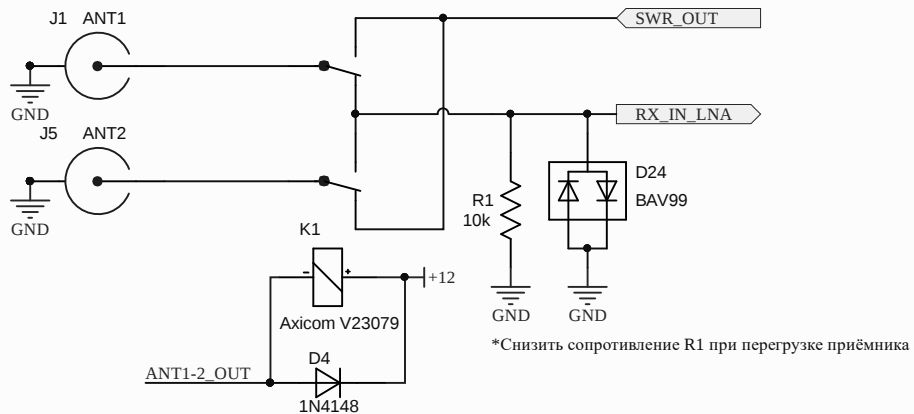


Title <i>LNA</i>			* * * * *	
Size: A4	Number:16	Revision:2.0		
Date: 15.04.2021	Time: 22:59:49	Sheet16 of 18		
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTUM DESIGNER\RF-UNIT\LNA.SchDoc				

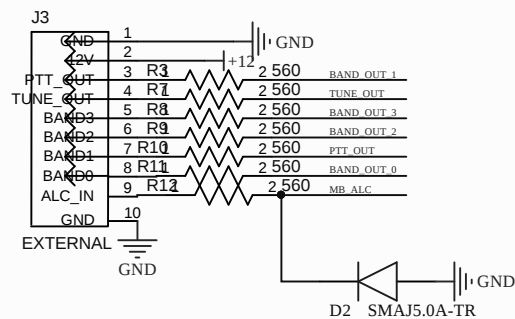
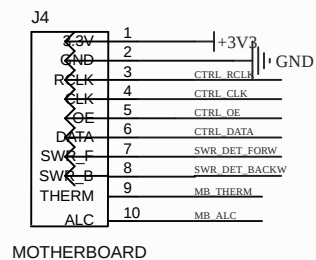
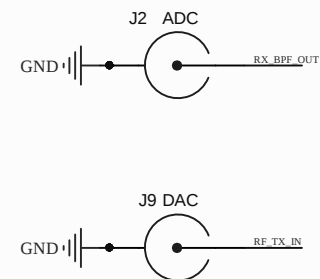


Title CONTROL			* * * *	
Size: A4	Number:15	Revision:2.0		
Date: 15.04.2021	Time: 22:59:51	Sheet 15 of 18		
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTUM_DESIGNER\RF-UNIT\CONTROL.SchDoc				

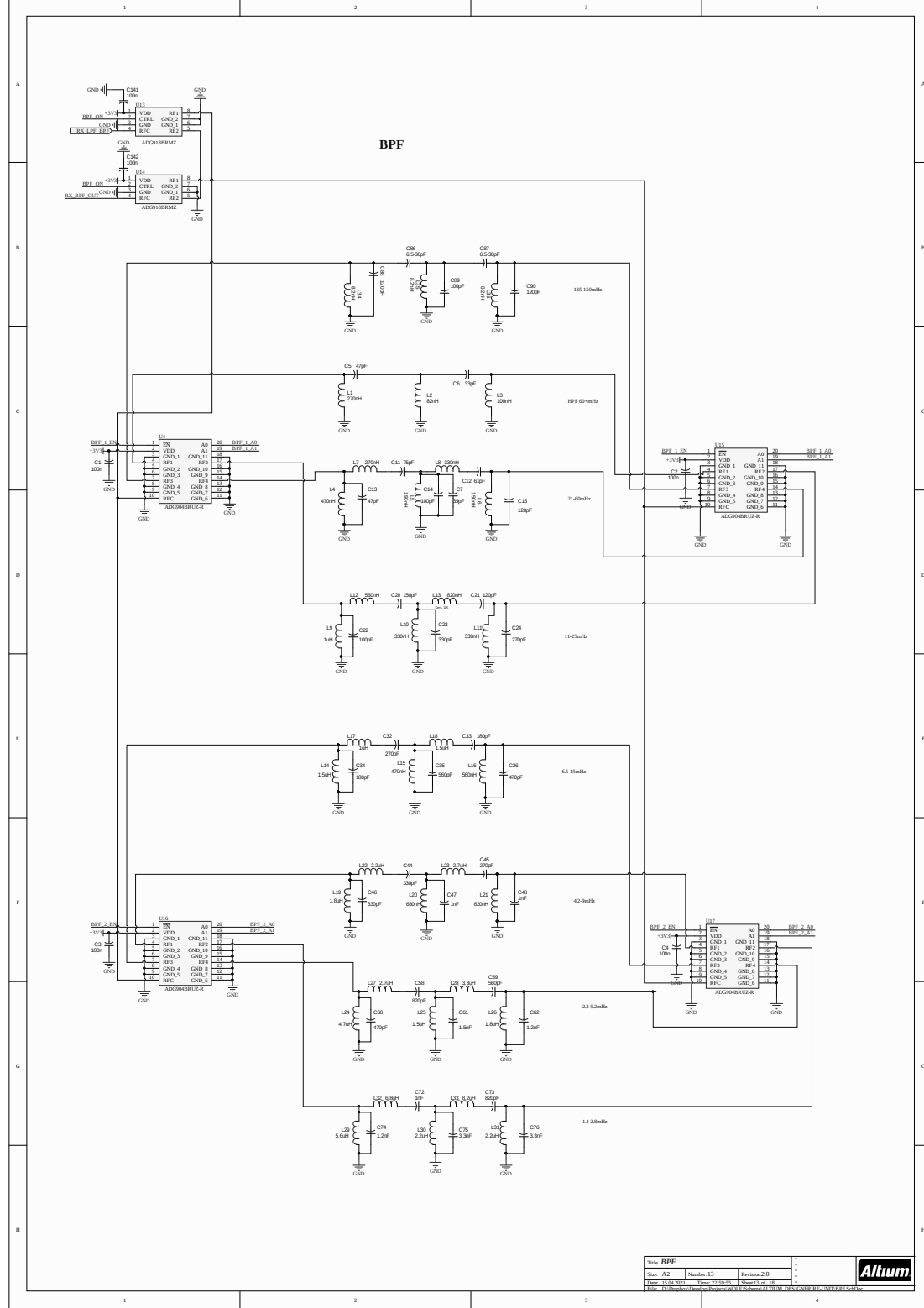
TXRX_COMUTATOR

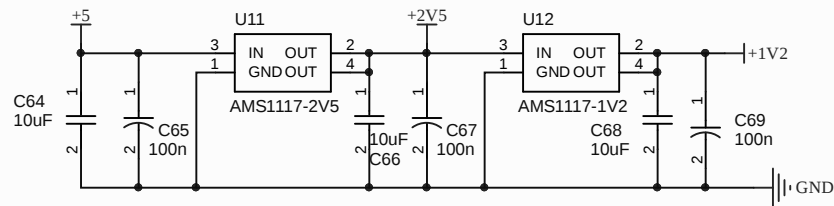
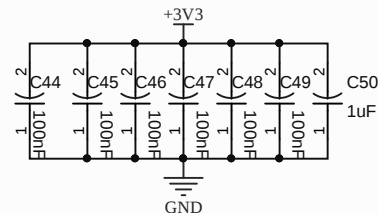
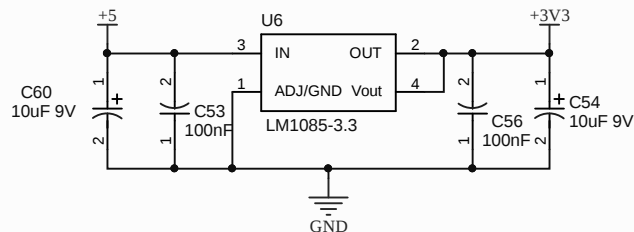
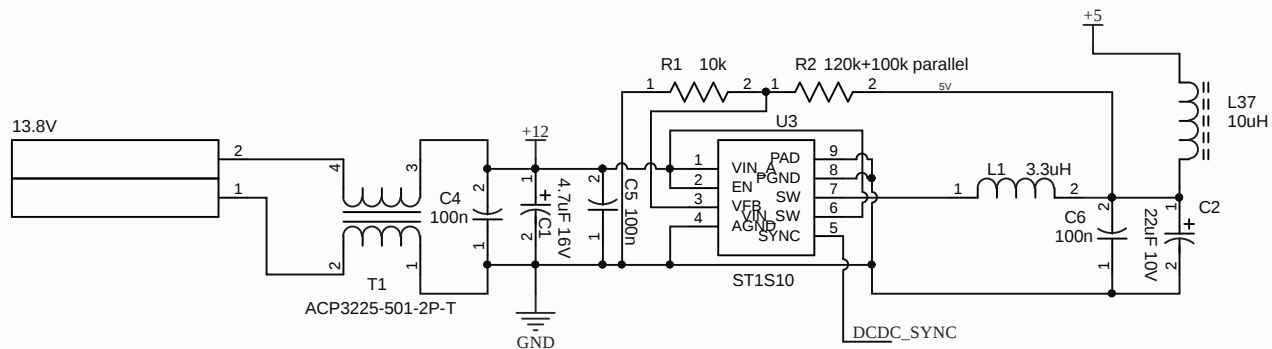


CONN

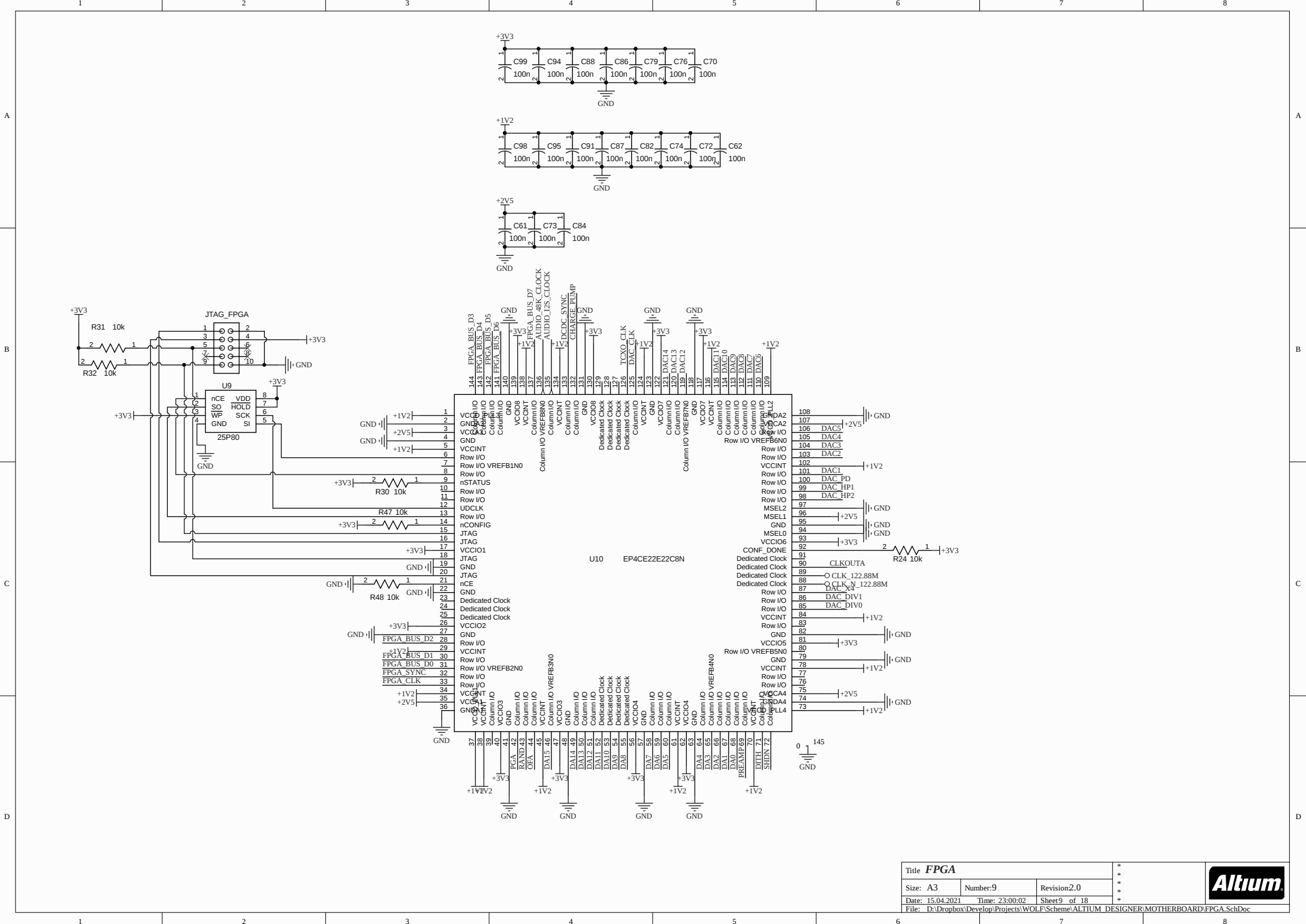


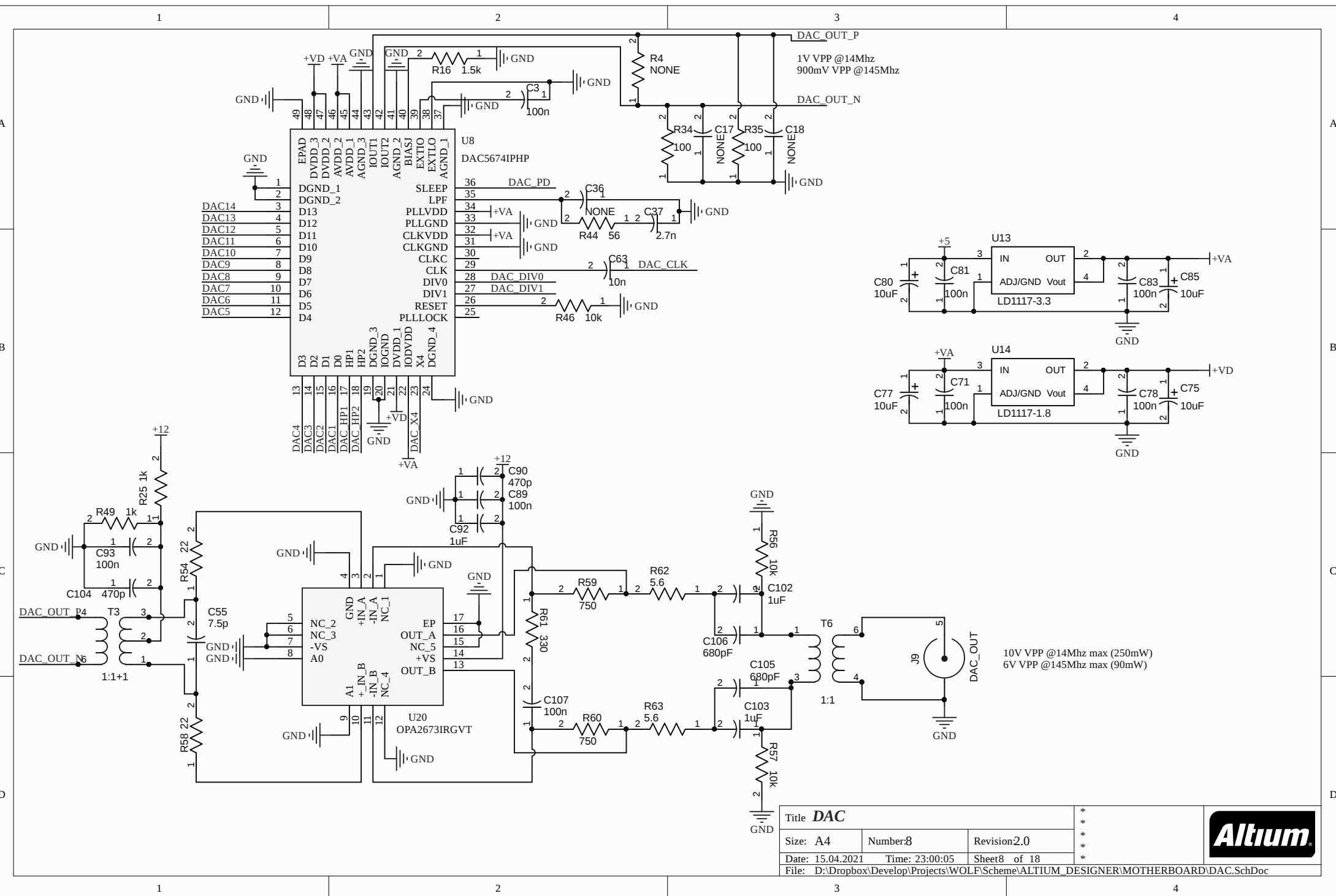
Title CONN			</
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Title POWER			
Size: A4	Number:10	Revision:2.0	
Date: 15.04.2021	Time: 23:00:00	Sheet10 of 18	
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTUM DESIGNER\MOTHERBOARD\POWER.SchDoc			





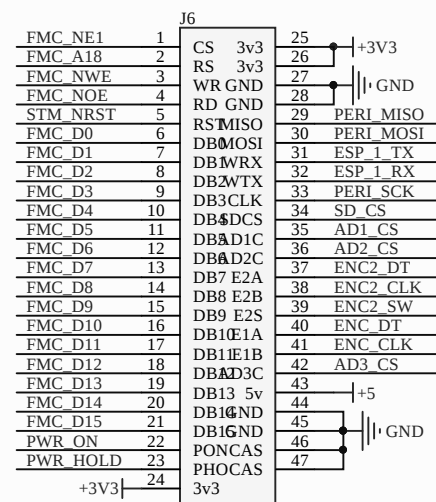
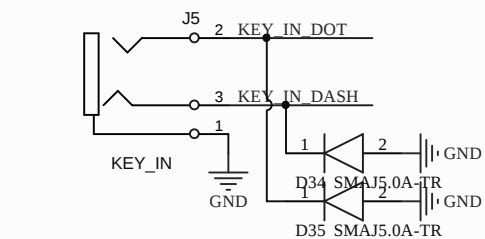
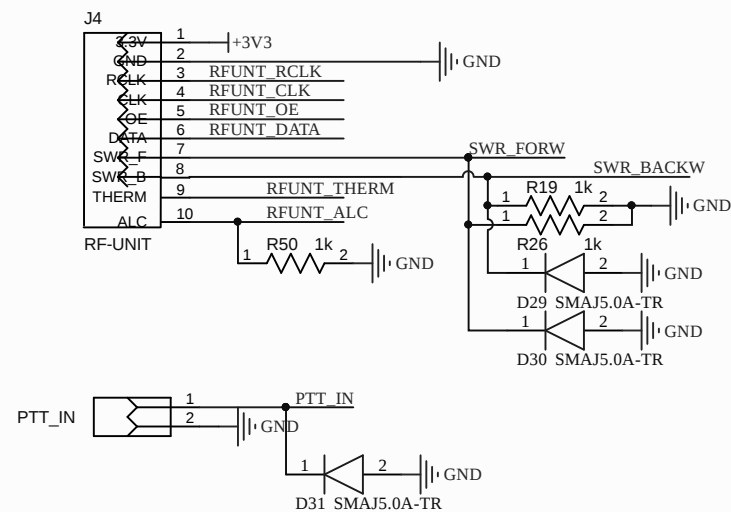
Title <i>DAC</i>			* * * * 
Size: A4	Number:8	Revision:2.0	
Date: 15.04.2021	Time: 23:00:05	Sheet8 of 18	
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTUM DESIGNER\MOTHERBOARD\DAC.SchDoc			

1

2

3

4



FPC-45P-0.5mm

Title **CONNECTIONS**

Size: A4

Number:7

Revision:2,0

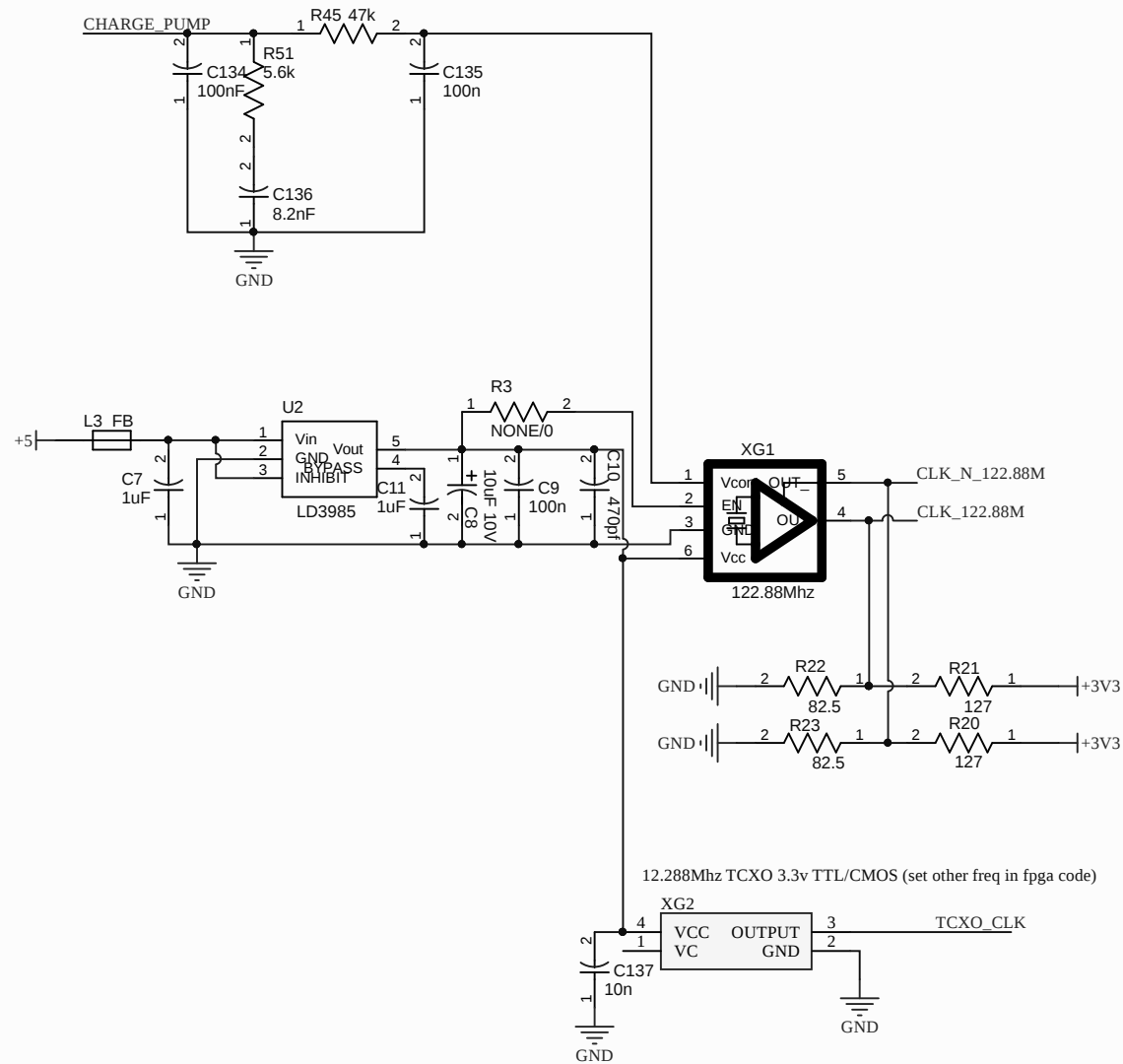
Date: 15.04.2021

Time: 23:00:07

Sheet 7 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTUM_DESIGNER\MOTHERBOARD\CONNECTIONS.SchDoc





Title **CLOCK_GENERATOR**

Size: A4

Number:6

Revision:2,0

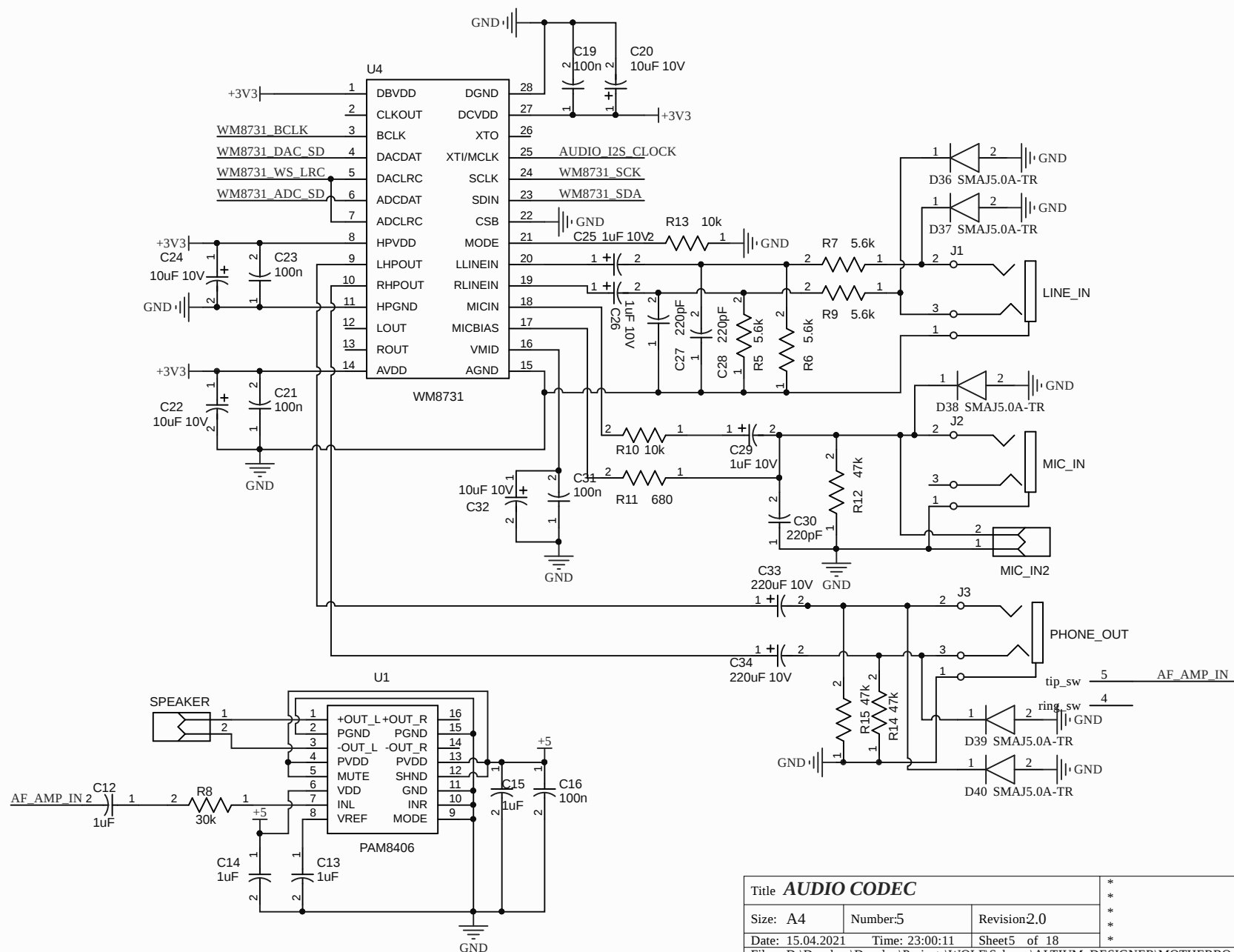
Date: 15.04.2021

Time: 23:00:09

Sheet6 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTUM_DESIGNER\MOTHERBOARD\CLOCK_GENERATOR.SchDoc



Title **AUDIO CODEC**

Size: A4

Number:5

Revision:2.0

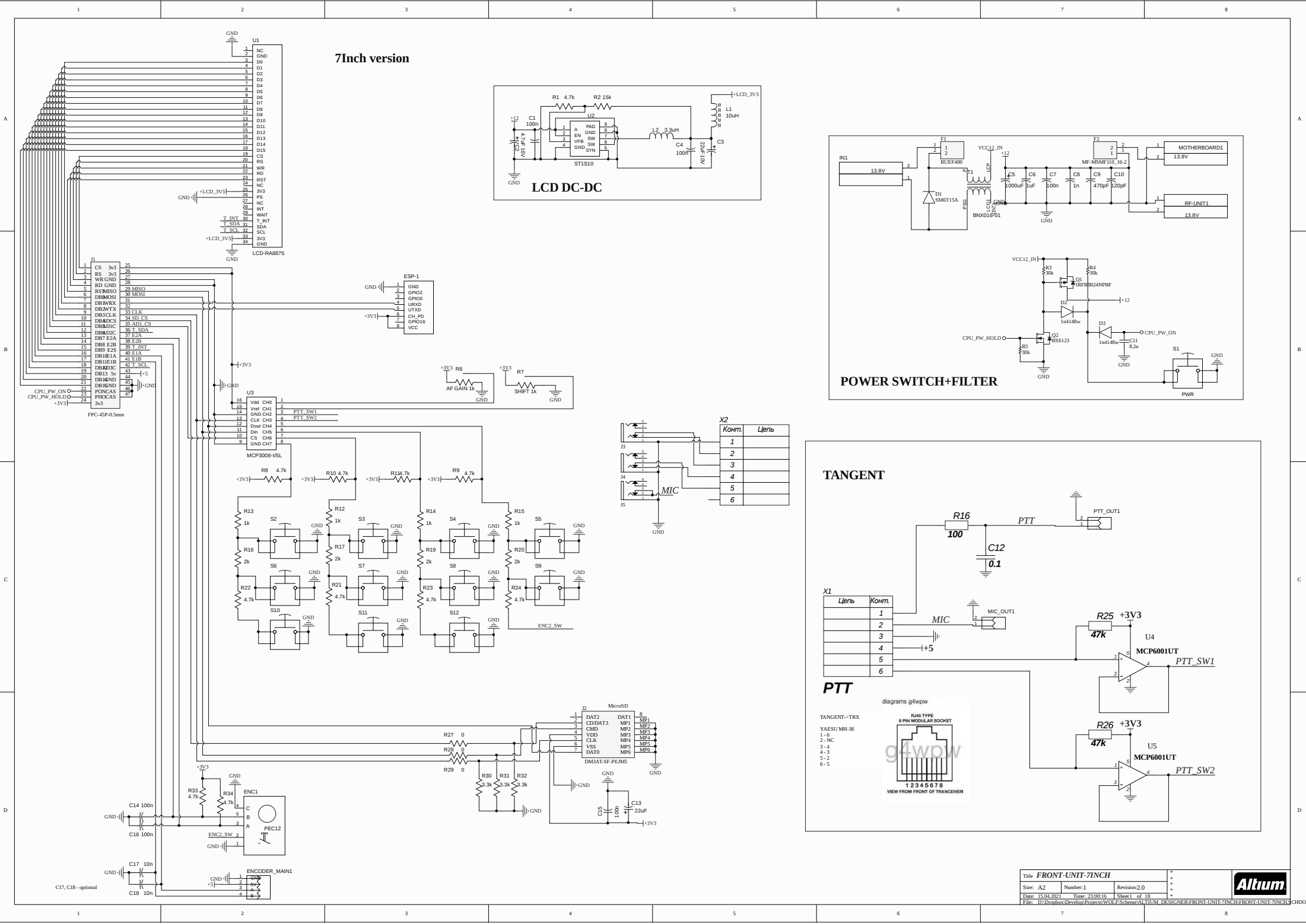
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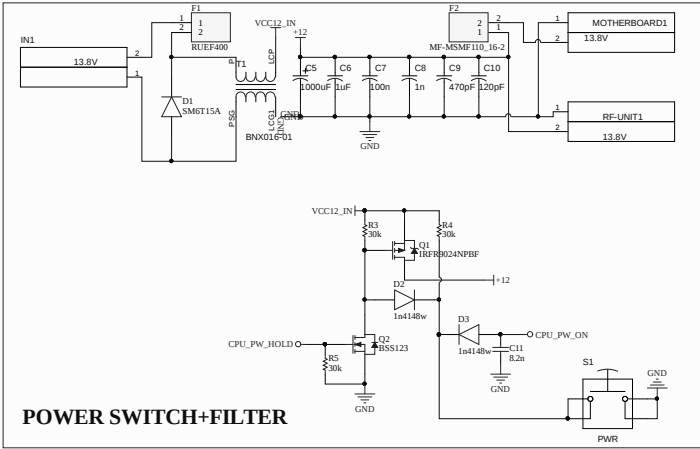
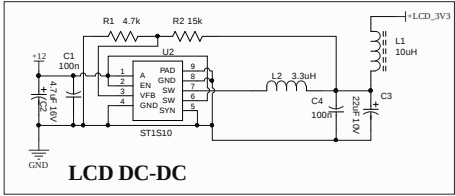
Sheet5 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTUM_DESIGNER\MOTHERBOARD\AUDIO CODEC.SchDoc





7Inch version



TANGENT

